

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Currently Amended) A logic circuit constituting a power module,
comprising:

a built-in main processor;

an interface for performing power consumption control; and

a function controlling consumption of power on another power module,

wherein said another power module includes an interface for outputting a request for raising or lowering an amount of power consumption of the logic circuit.

2. (New) The logic circuit having a built-in main processor according to claim 1, wherein the logic circuit further includes a clock signal output interface and said interface.